Demonstration of high saturation power/high gain SOAs using quantum well intermixing based integration platform

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In this letter we report the performance of semiconductor optical amplifiers (SOA) employing regions of high and low optical confinement designed for high saturation power and high gain using a novel quantum well (QW) intermixing (QWI) and MOCVD regrowth fabrication scheme. The scheme enables the monolithic integration of the high performance SOAs with high gain laser diodes, high efficiency electroabsorption modulators (EAM), and high saturation power photodiodes. The SOAs presented here exhibit saturation powers in the 19-20 dBm range with nearly 15 dB of gain.

Introduction: High functionality, high efficiency, and size reduction offered by single chip photonic circuits inspires the present effort towards increased levels of monolithic integration. The performance of the photonic integrated circuit (PIC) as a whole is ultimately limited by the performance of the individual components within the circuit. High functionality active PICs can require multiple QW band edges, differing gain or optical confinement at similar wavelengths, and radically different internal structures for components such as high saturation power photodiodes. Integration of the individual components within the PIC are essential for monolithic circuits to operate at the performance levels offered by circuits comprised of discrete components.

High-performance multiple QW band-edge widely-tunable transmitters have been demonstrated using our quantum well intermixing (QWI) integration platform [1]. With the addition of simple blanket MOCVD regrowth steps, we have extended the QWI integration platform to achieve QW active regions of both low and high optical confinement and high saturation power photodiodes operating at 40 Gb/s [2, 3]. In this work we exploit the capability of achieving both high and low optical confinement on a single chip for the demonstration of high saturation power/high gain SOAs.

Low optical confinement (LOC) MQW active regions are an attractive choice for employment in SOAs requiring high saturation power since the photon density within the QWs can be kept relatively low. Using the LOC-QW scheme, impressive saturation powers of +23 dBm and +28 dBm have been reported (4, 5). It should be noted that until recently the record SOA saturation power was +17 dBm (6). Here we report SOAs exhibiting 19-20 dBm saturation output powers with nearly 15 dB of chip gain using a fabrication scheme that will enable the SOAs to be placed in highly functional PICs containing widely tunable lasers, QW EAMs, and high saturation power photodiodes.

Experiment and Theory: Device fabrication initiates on an MOCVD grown epitaxial base structure consisting of ten 6.5 nm compressively strained QWs, separated by 8.0 nm tensile strained barriers, centered within two InGaAsP:Si (1.3Q:Si) layers designed to maximize the optical confinement at ~12.6% in the QWs. The samples are subjected to our QWI process as described in [7], in which the as-grown c-MQW is shifted in peak photoluminescence wavelength from 1530 nm to 1410nm in regions where passive waveguide, low optical confinement QWs, or high saturation power photodiodes are desired. An MOCVD regrowth is then performed for the growth of a thin InP:Si layer followed by a 1.3Q:Si stop etch layer, a 145nm InP:Si confinement tuning layer (CTL), a 5 o-MQW with similar compositions and thicknesses to that of the c-MQW, and a InP:Zn cap layer. Since the CTL layer functions to move the active wells away from the peak of the optical mode, the layer thickness is a key aspect in the design. In this work it was chosen such



Fig. 1 Side view schematic of the SOA device structure illustrating the c-MQW high modal gain region (left), and the o-MQW with reduced modal gain (right) for the realization of a high saturation power/ high gain SOA.

that the optical confinement factor in the o-MQW was ~1.3% to achieve saturation output powers in the 20 dBm range. Following the o-MQW regrowth, wet etching was used to selectively remove the o-MQW structure in regions it is not desired, a similar sequence was performed for the realization of high saturation power photodiodes [3], and the p-type InP:Zn cladding and p-contact InGaAs:Zn layers were grown. A thorough discussion of the theory and growth aspects can be found in [8]. Standard lithography methods were used to define 5 μ m wide surface ridge SOA devices with varying lengths. The three unique waveguide sections in the resulting SOA structures are shown in Fig. 1:

Results: The cleave back method was used to characterize Fabry Perot ridge lasers to extract the injection efficiency and optical loss of both active region types. The injection efficiency was found to be 70% and 73% in the c-MQW and o-MQW active regions, respectively. The optical loss was found to be 20 cm⁻¹ and 3 cm⁻¹ in the c-MQW and o-MQW active regions, respectively. A two-parameter fit was used to plot the threshold modal gain versus current density as shown in Fig. 2. The fit yielded a modal gain parameter of 94.1 cm⁻¹ in the o-MQW active region and 9.1 cm⁻¹ in the o-MQW active region, which is in good agreement with the simulated difference in optical confinement factor.

The SOA devices employed curved/flared passive input waveguides for reduced facet reflections. Single section low confinement SOAs with lengths of 1000µm and 1500µm along with a dual section SOA containing 150µm high optical confinement section followed by 1350µm of low optical confinement section were soldered to AIN carriers, wirebonded, and placed on a copper stage cooled to 18°C for characterization. A continuous wave (CW) 1550 nm light source was fed through a polarization controller and coupled into the SOA waveguide using a tapered fiber. The TE polarization state was used during all testing due to the polarization sensitivity of the compressively strained QWs. The coupled chip power was determined by reverse biasing the



Fig. 2 Experimental modal gain versus applied current density for 3µm wide Fabry Perot lasers employing high confinement c-MQW (diamonds) and low confinement o-MQW (squares) active regions.



Fig. 3 (a) Chip gain versus input power and (b) output power versus input power for SOA devices at 1550nm. The applied current density was 9 kA/cm^2 in the low- Γ o-MQW sections and 20 kA/cm^2 in the high- Γ c-MQW sections.

SOA and measuring the generated photocurrent. The long length of the SOAs was such that unity internal quantum efficiency could be assumed. The SOA was then forward biased at various current densities and the generated output power and chip gain were determined by the generated photocurrent in an integrated photodiode. The amplified spontaneous emission (ASE) was subtracted out such that it was not included in the output power or gain calculation.

The chip gain versus input power and output power versus input power characteristics are shown for the three SOA types in Fig. 3a and Fig. 3b, respectively. In all figures, the operating electrode current density was 9 kA/cm² in the o-MQW regions and 20 kA/cm² in the c-MQW regions. As can be seen in Fig. 3, a 1000µm long single section o-MQW device provides 6 dB of gain with a saturation output power of over 20 dBm while a 1500µm long single section device provides over 9 dB of chip gain with a saturation output power of 19.5 dBm. By placing a 150µm c-MQW high gain section in front of a 1350µm o-MQW section, the device gain is increased to nearly 15 dB while maintaining a saturation output power of over 19 dBm at 1550nm. Over 13.5 dB of gain was maintained for wavelengths from 1530 to 1560nm. Based on the results of this experiment, optimum o-MQW section lengths, c-MQW section lengths, and CTL thickness can be determined for improved device gain/saturation characteristics.

Conclusion: In this work we demonstrate high saturation power SOAs fabricated using an integration platform ideal for the monolithic integration of the SOAs with widely-tunable lasers, high performance QW EAMs, and high saturation power photodiodes. The SOAs presented here demonstrate saturation output powers in the 19-20 dBm range and chip gains of nearly 15 dB.

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